

Advanced Systems Lab

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Lecture: Optimizing FFT, FFTW

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Fast FFT: Example FFTW Library

www.fftw.org

Frigo and Johnson, FFTW: An Adaptive Software Architecture for the FFT, ICASSP 1998

Frigo, A Fast Fourier Transform Compiler, PLDI 1999

Frigo and Johnson, The Design and Implementation of FFTW3, Proc. IEEE 93(2) 2005

Recursive Cooley-Tukey FFT

$$\begin{aligned} \text{DFT}_{km} &= (\text{DFT}_k \otimes \text{I}_m) T_m^{km} (\text{I}_k \otimes \text{DFT}_m) L_k^{km} && \text{decimation-in-time} \\ \text{DFT}_{km} &= L_m^{km} (\text{I}_k \otimes \text{DFT}_m) T_m^{km} (\text{DFT}_k \otimes \text{I}_m) && \text{decimation-in-frequency} \end{aligned}$$

radix

For powers of two $n = 2^t$ sufficient together with base case

$$\text{DFT}_2 = \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$$

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Cooley-Tukey FFT, $n = 4$

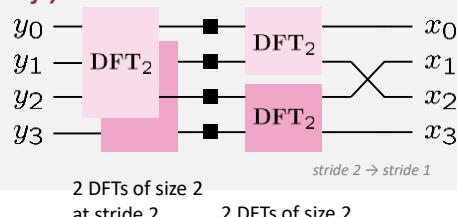
Fast Fourier transform (FFT)

$$\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & i & -1 & -i \\ 1 & -1 & 1 & -1 \\ 1 & -i & -1 & i \end{bmatrix} = \begin{bmatrix} 1 & \cdot & 1 & \cdot \\ \cdot & 1 & \cdot & 1 \\ 1 & \cdot & -1 & \cdot \\ \cdot & 1 & \cdot & -1 \end{bmatrix} \begin{bmatrix} 1 & \cdot & \cdot & \cdot \\ \cdot & 1 & \cdot & \cdot \\ \cdot & \cdot & 1 & \cdot \\ \cdot & \cdot & \cdot & i \end{bmatrix} \begin{bmatrix} 1 & 1 & \cdot & \cdot \\ 1 & -1 & \cdot & \cdot \\ \cdot & \cdot & 1 & 1 \\ \cdot & \cdot & 1 & -1 \end{bmatrix} \begin{bmatrix} 1 & \cdot & \cdot & \cdot \\ \cdot & \cdot & 1 & \cdot \\ \cdot & 1 & \cdot & \cdot \\ \cdot & \cdot & \cdot & 1 \end{bmatrix}$$

Representation using matrix algebra

$$\text{DFT}_4 = (\text{DFT}_2 \otimes \text{I}_2) \text{ diag}(1, 1, 1, i) (\text{I}_2 \otimes \text{DFT}_2) L_2^4$$

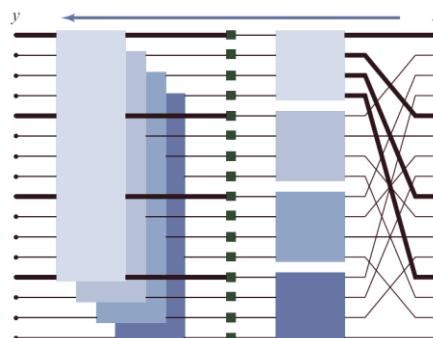
Data flow graph (right to left)



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FFT, $n = 16$ (Recursive, Radix 4)

$$\text{DFT}_{16} = \begin{matrix} & \text{DFT}_4 \otimes I_4 & T_4^{16} & I_4 \otimes \text{DFT}_4 & L_4^{16} \\ \text{DFT}_{16} & = & \begin{array}{c} \text{Matrix} \\ \text{DFT}_4 \otimes I_4 \end{array} & \begin{array}{c} \text{Matrix} \\ T_4^{16} \end{array} & \begin{array}{c} \text{Matrix} \\ I_4 \otimes \text{DFT}_4 \end{array} & \begin{array}{c} \text{Matrix} \\ L_4^{16} \end{array} \end{matrix}$$



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Fast Implementation (\approx FFTW 2.x)

Choice of algorithm

Locality optimization

Constants

Fast basic blocks

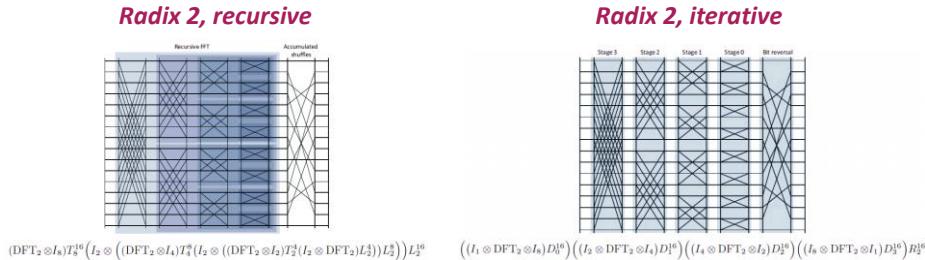
Adaptivity

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1: Choice of Algorithm

Choose recursive, not iterative

$$\text{DFT}_{km} = (\text{DFT}_k \otimes \text{I}_m) T_m^{km} (\text{I}_k \otimes \text{DFT}_m) L_k^{km}$$



First recursive implementation we consider in this course

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2: Locality Improvement

$$\text{DFT}_{16} = \begin{array}{c} \text{DFT}_4 \otimes \text{I}_4 \\ \text{I}_4 \otimes \text{DFT}_4 \\ T_4^{16} \\ L_4^{16} \end{array}$$

The diagram shows four components of a DFT16 implementation:
1. $\text{DFT}_4 \otimes \text{I}_4$: A 4x4 grid of blue squares.
2. T_4^{16} : A 4x4 diagonal matrix with black squares on the diagonal.
3. $\text{I}_4 \otimes \text{DFT}_4$: A 4x4 grid with a 2x2 block of blue squares at the bottom-right corner.
4. L_4^{16} : A 4x4 grid with black squares at specific positions.

Straightforward implementation: 4 steps

- *Permute*
- *Loop recursively calling smaller DFTs (here: 4 of size 4)*
- *Loop that scales by twiddle factors (diagonal elements of T)*
- *Loop recursively calling smaller DFTs (here: 4 of size 4)*

4 passes through data: bad locality

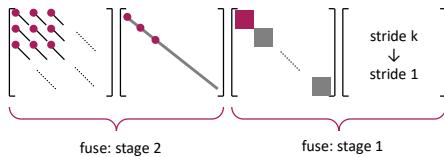
Better: fuse some steps

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2: Locality Improvement

$$DFT_n = (DFT_k \otimes I_m) T_m^n (I_k \otimes DFT_m) L_k^n$$

schematic:



fuse: stage 2

fuse: stage 1

- compute m many $DFT_k * D$ with input stride m and output stride m
- D is part of the diagonal T
- writes to the same location then it reads from → in-place
- compute k many DFT_m with input stride k and output stride 1
- writes to different location then it reads from → out-of-place

Interface needed for recursive call:

$DFTscaled(k, x, y, d, m);$

↑ ↑ ↑ ↑ ↑
DFT size input output diagonal elements output stride
 vector stride vector

Can handle further recursion
(just strides change)

Interface needed for recursive call:

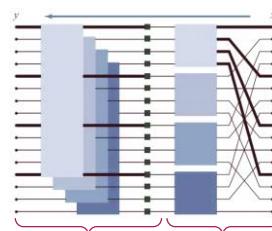
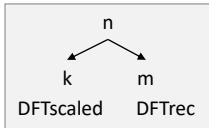
$DFTscaled(k, x, y, d, m);$

↑ ↑ ↑ ↑ ↑
DFT size input output diagonal elements output stride
 vector stride vector

Cannot handle further recursion so in
FFTWF it is a base case of the recursion

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$$DFT_{km} = \underbrace{(DFT_k \otimes I_m)}_{\text{one loop}} T_m^{km} \underbrace{(I_k \otimes DFT_m)}_{\text{one loop}} L_k^{km}$$



```
// code sketch
void DFT(int n, cpx *x, cpx *y) {
    ...
    int k = choose_dft_radix(n); // ensure k small enough
    int m = n/k;
    for (int i = 0; i < k; ++i)
        DFTrec(m, x + i, y + m*i, k, 1); // implemented as DFT(...) is
    for (int j = 0; j < m; ++j)
        DFTscaled(k, y + j, t[j], m); // always a base case
}
```

3: Constants

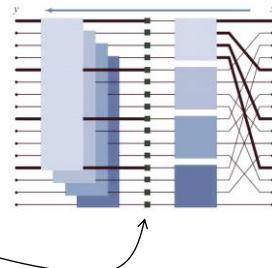
FFT incurs multiplications by roots of unity

In real arithmetic:

Multiplications by sines and cosines, e.g.,

$$y[i] = \sin(i \cdot \pi / 128) * x[i];$$

Very expensive!



Observation: Constants depend only on input size, not on input

Solution: Precompute once and use many times

```
d = DFT_init(1024); // init function computes constant table  
d(x, y);           // use many times
```

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4: Optimized Basic Blocks

```
// code sketch  
void DFT(int n, cpx *x, cpx *y) {  
    if (use_base_case(n))  
        DFTbc(n, x, y); // use base case  
    else {  
        int k = choose_dft_radix(n); // ensure k <= 32  
        int m = n/k;  
        for (int i = 0; i < k; ++i)  
            DFTrec(m, x + i, y + m*i, k, 1); // implemented as DFT(...) is  
        for (int j = 0; j < m; ++j)  
            DFTscaled(k, y + j, t[j], m); // always a base case  
    }  
}
```

Just like loops can be unrolled, recursions can also be unrolled

Empirical study: Base cases for sizes $n \leq 32$ useful (scalar code)

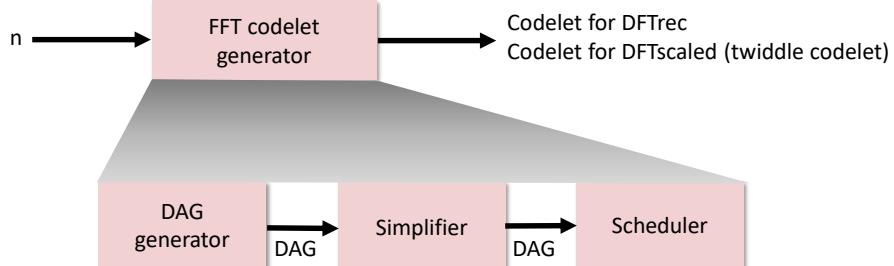
Needs 62 base cases or “codelets” (why?)

- *DFTrec*, sizes 2–32
- *DFTscaled*, sizes 2–32

Solution: Codelet generator (codelet = optimized basic block)

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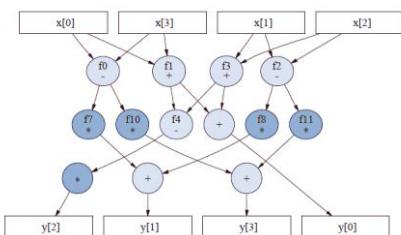
FFTW Codelet Generator



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Small Example DAG

DAG:



One possible unparsing:

```

f0 = x[0] - x[3];
f1 = x[0] + x[3];
f2 = x[1] - x[2];
f3 = x[1] + x[2];
f4 = f1 - f3;
y[0] = f1 + f3;
y[2] = 0.7071067811865476 * f4;
f7 = 0.9238795325112867 * f0;
f8 = 0.3826834323650898 * f2;
y[1] = f7 + f8;
f10 = (-0.9238795325112867) * f2;
y[3] = f10 + f11;

```

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DAG Generator



Knows FFTs: Cooley-Tukey, split-radix, Good-Thomas, Rader, represented in sum notation

$$y_{n_2j_1+j_2} = \sum_{k_1=0}^{n_1-1} (\omega_n^{j_2 k_1}) \left(\sum_{k_2=0}^{n_2-1} x_{n_1 k_2 + k_1} \omega_{n_2}^{j_2 k_2} \right) \omega_{n_1}^{j_1 k_1}$$

For given n, suitable FFTs are recursively applied to yield n (real) expression trees for outputs y_0, \dots, y_{n-1}

Trees are fused to an (unoptimized) DAG

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Simplifier



Applies:

- Algebraic transformations
- Common subexpression elimination (CSE)
- DFT-specific optimizations

Algebraic transformations

- Simplify mults by 0, 1, -1
- Distributivity law: $kx + ky = k(x + y)$, $kx + lx = (k + l)x$
Canonicalization: $(x-y)$, $(y-x)$ to $(x-y)$, $-(x-y)$

CSE: standard

- E.g., two occurrences of $2x+y$: assign new temporary variable

DFT specific optimizations

- All numeric constants are made positive (reduces register pressure)
- CSE also on transposed DAG

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Scheduler



Determines in which sequence the DAG is unparsed to C
(topological sort of the DAG)

Goal: minimize register spills

A 2-power FFT has an operational intensity of $I(n) = O(\log(C))$, where C is the cache size [1]

Implies: For R registers $\Omega(n \log(n)/\log(R))$ register spills

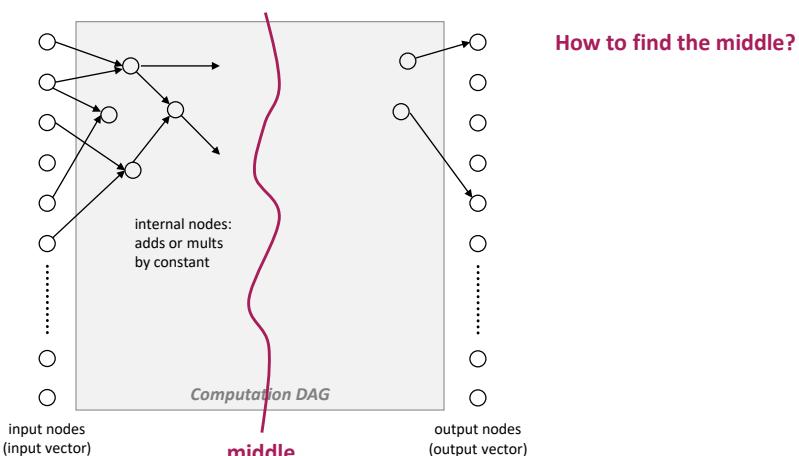
FFTW's scheduler achieves this (asymptotic) bound *independent* of R

[1] Hong and Kung: "[I/O Complexity: The red-blue pebbling game](#)"¹⁷

FFT-Specific Scheduler: Basic Idea

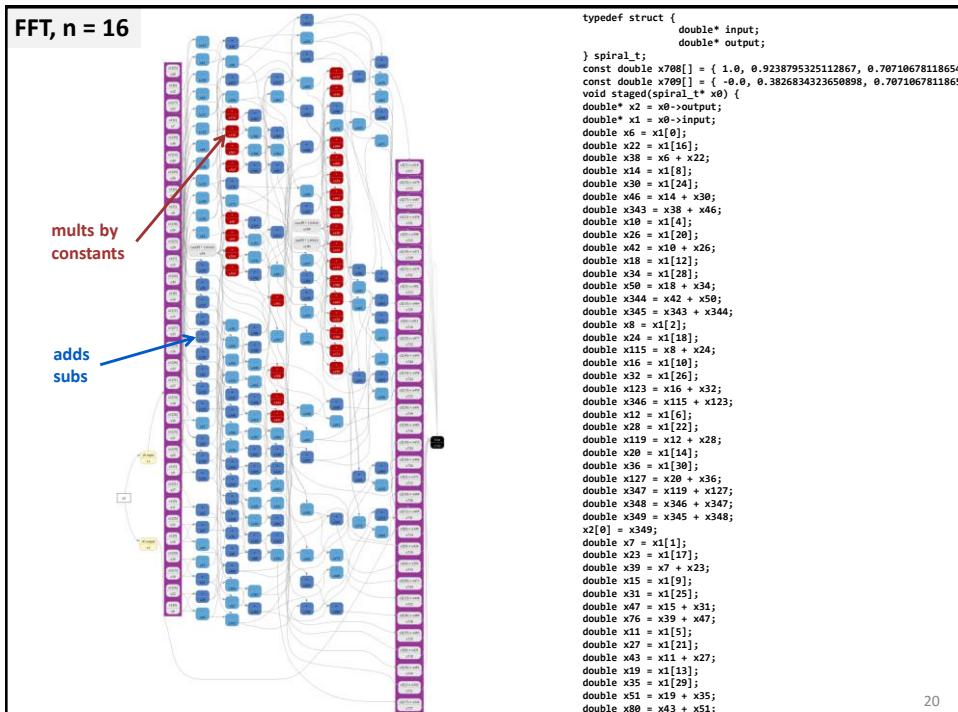
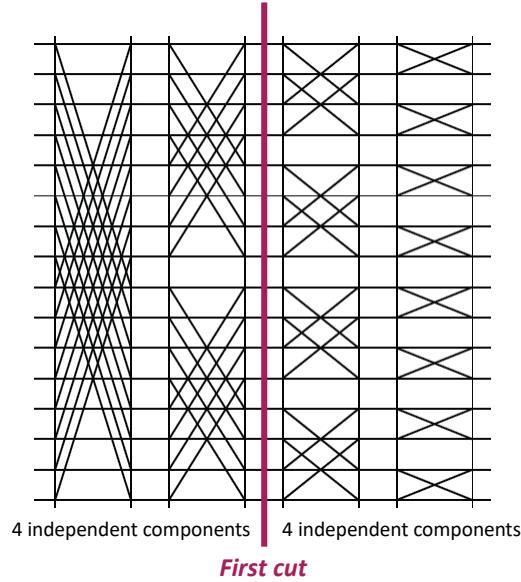
Cut DAG in the middle

Recurse on the connected components



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This is a Sketched/Abstracted DAG



Codelet Examples

[Notwiddle 2 \(DFTrec\)](#)

[Notwiddle 3 \(DFTrec\)](#)

[Twiddle 3 \(DFTscaled\)](#)

[Notwiddle 32 \(DFTrec\)](#)

Code style:

- *Single static assignment (SSA)*
- *Scoping (limited scope where variables are defined)*

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5: Adaptivity

```
// code sketch
void DFT(int n, cpx *x, cpx *y) {
    if (use_base_case(n)) // use base case
        DFTbc(n, x, y);
    else {
        int k = choose_dft_radix(n); // ensure k <= 32
        int m = n/k;
        for (int i = 0; i < k; ++i)
            DFTrec(m, x + i, y + m*i, k, 1); // implemented as DFT(...) is
        for (int j = 0; j < m; ++j)
            DFTscaled(k, y + j, t[j], m); // always a base case
    }
}
```

Choices used for platform adaptation

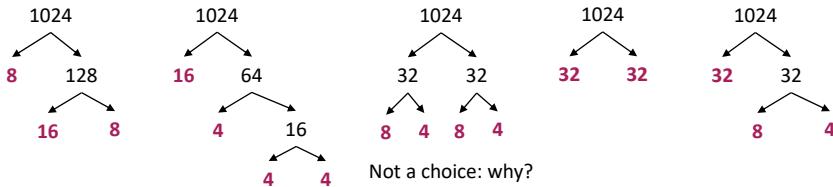
```
d = DFT_init(1024); // compute constant table; search for best recursion
d(x, y); // use many times
```

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5: Adaptivity

```
d = DFT_init(1024); // compute constant table; search for best recursion  
d(x, y); // use many times
```

Choices: $DFT_{km} = (DFT_k \otimes I_m)T_m^{km}(I_k \otimes DFT_m)L_k^{km}$



Base case = generated codelet is called

Exhaustive search to expensive

Solution: Dynamic programming

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FFTW: Further Information

Previous Explanation: FFTW 2.x

FFTW 3.x:

- *Support for SIMD/threading*
- *Flexible interface to handle FFT variants (real/complex, strided access, sine/cosine transforms)*
- *Complicates significantly the interfaces actually used and increases the size of the search space*

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	MMM <i>Atlas</i>	Sparse MVM <i>Sparsity/Bebop</i>	DFT <i>FFTW</i>
Cache optimization			
Register optimization			
Optimized basic blocks			
Other optimizations			
Adaptivity			

	MMM <i>Atlas</i>	Sparse MVM <i>Sparsity/Bebop</i>	DFT <i>FFTW</i>
Cache optimization	Blocking	Blocking (rarely useful)	Recursive FFT, fusion of steps
Register optimization	Blocking	Blocking (changes sparse format)	Scheduling of small FFTs
Optimized basic blocks	Unrolling, scalar replacement and SSA, scheduling, simplifications (for FFT)		
Other optimizations	—	—	Precomputation of constants
Adaptivity	Search: blocking parameters	Search: register blocking size	Search: recursion strategy